

Application Number: 10/082,852  
 Filing Date: 2-25-02  
 First Named Inventor: James D. Beasom  
 Group Art Unit: 2812  
 Examiner Name: \_\_\_\_\_  
 Attorney Docket: INT-0009A

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PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

U.S. Patent Documents

Examiner Initials	Cite No.	U.S. Patent Number	Kind Code	Name of Patentee or Applicant of Cited Document	Date of Publication	Pages, Columns, Lines Where Relevant Passages or Figures Appear
		5,652,153		Beasom	7-1997	
		6,225,181		Gregory	5-2001	
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		5,633,180		Bajor	5-1997	
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Examiner Initials	Cite No.	Office	Number	Kind Code	Name of Patentee or Applicant	Date of Publication MM-DD-YY	Location of Relevant Passages/Figures	T

Other Prior Art - Non Patent Literature Documents

Examiner Initials	Cite No	Name of Author (All Caps), Title of Article/Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country Where Published	T
		Sze, S.M. "Semiconductor Devices, Physics and Technology" John Wiley & Sons, Pg 384	

Examiner Signature	Date Considered

**\*\*EXAMINER:** Initial citation considered. Draw line through citation if no in conformance and not considered. Include copy of this form with next communication to application.

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Foreign Patent Documents

Examiner Initials	Cite No.	Office	Number	Kind Code	Name of Patentee or Applicant	Date of Publication MM-DD-YY	Location of Relevant Passages/Figures	T

Other Prior Art - Non Patent Literature Documents

Examiner Initials	Cite No	Name of Author (All Caps), Title of Article/Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country Where Published	T
		RUPIT PATEL, et al., A 30V Complementary Bipolar Technology on SOI for High Speed Precision Analog Circuits, 1997 IEEE BCTM, pgs. 48-50.	
		MC WILSON, et al., Process HJ: A 30 GHz NPN and 20 GHz PNP Complementary Bipolar Process for High Linearity RF Circuits, 1998 IEEE BCTM, pgs. 164-167.	

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